



[1] Sylvain Collange, David Defour, David Parelo, Barra, a Modular Functional GPU Simulator for GPGPU

[2] Gregory Diamos, The Design and Implementation Ocelot's Dynamic Binary Translator from PTX to Multi-Core x86

[3] John Stratton, Sam Stone, Wen-Mei Hwu, MCUDA: An Efficient Implementation of CUDA Kernels for Multi-Core CPUs